# LabVIEW Based Implementation of Resistive Temperature Detector Linearization Techniques

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*Abstract*—Resistance temperature detectors (RTDs) are highquality temperature sensors used for accurate temperature measurements and ideally suited for industrial applications, but their non-linearity is a serious drawback in temperature monitoring in which precise measurement and control are crucial. In this paper, two linearization techniques are implemented in LabVIEW environment involving voltage divider and feedback compensation circuits. The presented techniques considerably decrease the effects of non-linearity and may accommodate temperature variations).

## Keywords-RTD; LabVIEW; Wheatstone bridge; instrumentation amplifier; simulation; sensor

## I. INTRODUCTION

Resistance temperature detectors (RTDs), as their name implies, are sensors that measure the temperature by correlating the resistance of the RTD element with it. Most RTD elements consist of a certain length of very thin wire surrounded by a ceramic or glass core [1, 2]. The element is usually quite fragile, and it is often placed inside a sheathed probe for protection. The RTD element is made from a pure material, whose resistance to different temperatures has been attested [3, 4]. RTD is one of the most accurate temperature sensors. It offers a good level of accuracy and is extremely stable and reliable. RTDs are also relatively insensitive to electrical noise and therefore well suited for temperature measurements in industrial environments, especially around motors, generators and other high voltage equipment. The resistance of an RTD sensor increases with the temperature by about 0.39%/°C, but they have a significant non-linearity of about 0.38% for an interval of 100°C [5]. This non-linearity is often numerically corrected, but there are many applications to correct it analogically. RTDs can be used for many industrial applications because they are inexpensive. However, their nonlinear resistance characteristic makes accurate temperature conversion complex. To minimize this difficulty, the RTD's resistance characteristic should be linearized by placing a thermistor within a resistor divider or with feedback compensation. This linearized response is simpler to convert to a temperature value than the thermistor's raw response. To obtain an optimum level of linearity, a pair of formulas can be derived as will be explained below. These formulas are useful in determining component values for this common linearization circuit. When the RTD is excited with a voltage source, the resulting RTD voltage is directly proportional to the resistance, producing the same nonlinearity. However, with the use of a resistor divider, Wheatstone bridge, or feedback compensation, the non-linearity can be significantly reduced, and this linearization technique can be applied to other resistive sensors, mainly with second-order non-linearity. The law of variation of the resistance with the temperature for the RTD sensors is given by the formula of approximate relation [6]:

$$R_{TD}(T) = R_0 \left(1 + AT + BT^2 + C(T - 100)T^3\right)$$
(1)

where  $A=3.908\times10^{-3}$ ,  $B=-5.802\times10^{-7}$  and C=0 for T<0.

To obtain an RTD resistance that varies almost linearly with temperature, two linearization methods are presented in this paper using the Laboratory Virtual Instrument Engineering Workbench (LabVIEW) environment which allows developing a program for the design and analysis of the RTD linearization circuits [7-9]. Firstly, a voltage divider can be used by placing a resistor R in series with the RTD. Then, a Wheatstone bridge with feedback compensation is implemented. The bridge output varies in a non-linear manner with a variation of temperature of the process parameter, and this non-linearity factor can be eliminated by incorporating feedback in the bridge amplifier or by software means [10]. But for both methods, the combination of resistance tolerance and temperature coefficient defines the resistance against the temperature characteristics for the RTD sensor. The greater the tolerance of the element, the more the sensor will spread a generalized curve, and the more variation it will be to the sensor. This is important for users who need to change or replace sensors and want to minimize systematic errors. This work may offer a beneficial base for engineers who are trying to reduce the cost and time of RTD-related circuits and can also be applied for high precision temperature sensing in MRI system applications [11-14].

## II. LINEARIZATION TECHNIQUES

## A. Linearization with Voltage Divider Method

To obtain an output voltage  $V_o$  that varies almost linearly with temperature, a voltage divider can be made by placing a

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resistor R in series with the RTD [15]. However, the major problem is how to determine the optimal resistance value that will attain the expected linear range. The ratio between the output and the input voltage can be expressed as:

$$\frac{V_0}{V_i} = g(T) = \frac{1}{1 + m x(T)}$$
 (2)

where *m* is the ratio of  $R_o$  to *R*, and x(T) is the ratio of  $R_{TD}$  to  $R_o$ 

The function g(T) is represented in Figure 1 in terms of temperature for different values of m. The expected temperature range is from 0°C to 180°C.



Fig. 1. Curves of g(T) for different values of m

It is clear from Figure 1 that as the factor *m* increases, the linear region decreases and gets limited. The appropriate value of *m* can be determined to increase the linear region using the second derivative of the function g(T) that can be solved as:

$$\frac{d^2g(T)}{dT^2} = \frac{-\frac{dx(T)}{dT}}{(1+m\,x(T))^2}$$
(3)

to get a straight-line response for g(T). The appropriate value of m can be found from the following expression:

$$m = \frac{2B}{(1+AT+BT^2)[(A+2BT)^2 - 2B]}$$
(4)

## B. RTD Linearization with Feedback Compensation

One of the methods to avoid the non-linearity problem is the use of bridge measurement. Wheatstone bridge measurement is an indirect indication of the resistance of the RTD. The bridge requires four connecting wires, an external source, an amplifier, and three resistors that have a temperature coefficient of zero. To avoid subjecting the other three resistors of the bridge to the same temperature, the RTD is separated from the bridge by a pair of connecting wires. These wires recreate the problem of non-linearity: The impedance of the connecting wires affects the reading of the temperature. This effect can be minimized by choosing a large value for the ratio of R1 to R3. However, the sensitivity of the bridge should be decreased, resulting in low bridge output and hence, low resolution. The bridge output voltage  $V_o$  can be expressed as:

$$Vo = A Vin\left(\left(\frac{R_{TD}}{R_{TD}+R_2}\right) - \left(\frac{R_3}{R_3+R_1}\right)\right)$$
(5)

where  $R_{TD}$  is the resistance of the RTD at temperature T,  $V_{in}$  is the input voltage, and A is the gain of the amplifier.

Rearranging (5), the output voltage is expressed in terms of the ratio *R1* to *R3* as:

$$Vo = A \operatorname{Vin}\left(\left(\frac{R_{TD}}{R_{TD} + R^2}\right) - \left(\frac{1}{1 + \left(\frac{R_1}{R_3}\right)}\right)\right)$$
(6)

Thus, the simple Wheatstone bridge creates a non-linear relationship between the resistance change and the bridge voltage change, which requires a feedback compensation scheme that can be used for the linearization of the bridge output. The proposed diagram is shown in Figure 2. The bridge output  $V_{ob}$  is amplified by an instrumentation amplifier of gain A to generate the final output, Vo. A part of the output is added to a reference voltage  $V_r$  using a unity gain adder, and the output of the adder delivers the source excitation,  $V_{in}$  [16].



Fig. 2. Feedback compensation configuration

It is clear from the feedback compensation circuit that the output voltage  $V_0$  is expressed as:

$$Vo = A Vob = A (Vr + Vg) \left( \left( \frac{R_{TD}}{R_{TD} + R2} \right) - \left( \frac{1}{1 + \left( \frac{R_1}{R_3} \right)} \right) \right) (7)$$

where Vg=(R5/(R5+R4)) Vo, and Vr is the reference voltage. Rearranging (7), the output voltage  $V_0$  is expressed as:

$$Vo = A \left( Vr + \frac{R5}{R5 + R6} Vo \right) \left( \left( \frac{R_{TD}}{R_{TD} + R2} \right) - \left( \frac{1}{1 + \left( \frac{R1}{R3} \right)} \right) \right)$$
(8)

If the factor of attenuation  $\frac{R5}{R5+R6}$  is chosen such that

 $\frac{AR5}{R5+R6}$  equals to  $\frac{R1}{R3}$  + 1 then (8) implies that the feedback scheme of Figure 4 provides an amplified output, linearly proportional to the temperature expressed as:

$$Vo = A Vr \frac{\frac{R_1}{R_3}}{\left(1 + \left(\frac{R_1}{R_3}\right)\right)^2} (1 + \alpha T)$$
(9)

Figrue 3 shows the output voltage as a function of temperature for different values of the R1/R3 ratio.

A series of simulations were carried out in Labview to determine the efficacy of the proposed method and are detailed below.



Fig. 3. Output Voltage curves for different values of R1/R3

## III. SOFTWARE DEVELOPMENT AND SIMULATION RESULTS

The virtual instrument concept that gave LabVIEW its name is manifested by the permanence of a graphical interface for each module (function) of a program. The controls and indicators on the front panel are the interfaces through which the program interacts with the user (reading commands and parameters, displaying results). The control-command functions of cards or instruments are the interface through which the program interacts with the editing [7]. Both presented linearization techniques were implemented and incorporated into a virtual instrument on the LabVIEW environment. Figure 4 shows the block diagram of the implementation process.



Fig. 4. Block diagram of the implementation process

The implementation includes the specified and defined RTD character and the expected temperature range. Also, by the other described conditions, an optimal linear region or solution for linearization can be obtained. Finally, the "waveform graph" output on the front panel can display simulation results and temperature plots in the expected range. Figures 5 and 6 show the block diagram and the front panel (temperature waveform graph) that follows the divider voltage model presented in Figure 1. In this block diagram, three sub virtual instruments (SubVis) are created to specify the RTD character and perform the required calculations (optimal value of R and excepted linear region). The program gives the optimal value of R is 50k for linearizing a RTD over the range of 0-180°C. Figures 7 and 8 show the block diagram and the interactive user interface of the VI (front panel of the system) that follows the feedback compensation model presented in Figure 3.



Fig. 5. Block diagram: Voltage divider method

It is clear from Figure 8 that we can get a straight-line curve of RTD. This linearity is good relatively to the one obtained with the divider voltage method. Simulation results show that the non-linearity decreases when the value of amplifier gain increases up to a certain limit. After that limit, non-linearity increases and the change will be on the value of the output voltage since the amplifier gain is multiplied by it and then the relation between temperature and Voltage will be changed as shown in Figure 9.



Fig. 6. Waveform graph of the output Voltage in terms of temperature for divider voltage circuit



Fig. 7. Block diagram: Feedback compensation method



Fig. 8. Interactive user interface for feedback compensation method



Fig. 9. Output Voltage curves for two values of amplifier gain A with feedback compensation.

## IV. CONCLUSION

RTD linearization has been implemented in LabVIEW, using divider voltage and feedback compensation methods. A two-parameter model for RTD was studied and presented to simulate and implement linearization techniques within an expected temperature range from 0°C to 500°C. The linearization error was estimated to be less than 0.5°C. By comparison with a resistance RTD without linearization, this represents an improvement of around 25%, which is very important for industrial applications like thermal compensation and room temperature measurement. As a final conclusion, LabVIEW is an effective tool for analyzing RTD to define an optimal value for a resistor used in RTD linearization.

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