Highly Stable Photonic Local Carriers for Phased Array Receiver System

Md. Rezaul Hoque Khan Islamic University of Technology, Dhaka, Bangladesh and University of Twente, Netherlands rhkhan@iut-dhaka.edu

Abstract-In this paper, a complete system analysis of photonic local carrier generation technique has been investigated. The generated carrier is potentially suitable to replace the existing microwave/RF Local Carrier (LC) used in commercial Low Noise Blocks (LNBs) for the Phased Array (PA) receiver system. The optical LC generated from heterodyning of two commercialized lasers is being stabilized with an Optical Frequency Lock Loop (OFLL). This approach resulted in a generated carrier at the K_uband (10.7GHz to 12.75GHz) signal received from a PA receiver. Various loop parameters of the OFLL have been investigated to comply with the requirements of the commercial LNBs The proposed OFLL shows a 2400 fold improvement in the frequency stability at 1000s averaging time compared to its free running condition. It is also demonstrated that with an optimized loop gain of 30dB, the loop response time of the proposed OFLL becomes 11µs.

Keywords-Optical Frequency Lock Loop (OFLL); microwave carrier generation; locking range; frequency stability

I. INTRODUCTION

Stabilization of laser frequency differences is essential in many modern experimental schemes. Applications extend from advanced optical fiber telecommunications in atomic clocks or in high resolution atomic and molecular spectroscopy [1-4] to precision spectroscopy and sensing [5, 6]. Phase coherence of the two laser fields locked at a frequency offset is not required in many applications and a mere frequency lock is an adequate solution. In any case, the Optical Phase Lock Loop (OPLL) is a standout amongst the most commonly utilized locking techniques. When the OPLL is designed to ensure only the stabilization of the frequency drift and not the consistency of the phase, it is called Optical Frequency Locked Loop (OFLL). In all these applications, a precisely defined optical frequency is needed and long-term stability must be ensured for correct system operation, with a required degree of stability and accuracy that depends on the application. Practically speaking, a beat signal is produced between the laser to be frequencystabilized (slave) and a second laser with known frequency (master) [7]. To stabilize the optically generated carrier, the beat signal is often mixed down to a lower frequency which is far better to be taken care of electronically [8, 9]. The main difference among the various frequency locking schemes which have been developed in the last decade, is the method used to generate and process the error signal employed for locking the

Md. Ashraful Hoque Islamic University of Technology Dhaka, Bangladesh mahoque@iut-dhaka.edu

slave laser [10]. A very simple locking scheme was demonstrated in [11] using an electronic delay line in conjunction with a phase detector as a frequency dependent phase shifter. This scheme has the advantage of large capture range, but as a drawback the beat frequency must be tuned by a manual adjustment of the delay line length. As a consequence, real-time, rapid tuning of the beat frequency is impossible. Other methods used to generate beat frequency use frequency multiplier. A different approach converts the beat frequency to a proportional voltage by a Frequency-to-Voltage Converter (FVC). The voltage is then compared to a reference voltage, which sets the beat frequency [12]. This approach is limited to the maximum operating frequency of the commercial FVC and can be improved by using a hybrid analog-digital locking scheme using high performance FVCs [13]. Nevertheless the scheme suffers from the limited bandwidth of the FVC. Recently, a highly stable and wide tunable frequency locking scheme has been presented, where a high-speed frequency divider (prescaler) is used on the beat signal before processing the error signal [14]. The main disadvantage of the use of the prescaler is that it degrades the loop response time and limits the loop bandwidth. Generating the error signal from the amplitude response of an RF filter to realize a sensitive analog FVC is also proposed [8, 15]. An alternative of these modulation-free error conversion techniques is the use of electrical filters [16] or frequency multipliers [17].

However, to the best of our knowledge, none of the above schemes reported any investigation on the long term frequency stability of the beat spectrum of the OFLL. A simple OFLL technique based on the concept presented in [11] is proposed in this paper and its performance is improved with the use of a variable delay line in the frequency discriminator to facilitate beat frequency tuning.

II. PROPOSED OPTICAL FREQUENCY LOCKED LOOP SCHEME

The frequency of a diode laser depends on the injection current and the temperature and is very sensitive to fluctuations of those parameters. For example, the DFB laser (Avanex inc., A1905LMI) used in our experiment has a frequency sensitivity to injection current and temperature of 325MHz/mA and 10GHz/⁰C respectively [18]. Several studies and experiments show that the beat signal generated by a free-running heterodyning system suffers a substantial frequency drift [19]. Laser frequency drifts by hundreds of MHz in an ordinary

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Corresponding author: M. R. H. Khan

environment [20]. The maximum permissible frequency drift of the generated carrier is 5MHz [21], according to the DVB-S specification [22]. The carrier signal, generated by heterodyning of two lasers, is used for the downconversion of the 10.7GHz to 12.75GHz K _u-band signal received from a Phased Array Antenna (PAA). The block diagram of the proposed OFLL is presented in Figure 1. The proposed scheme is able to generate an adjustable carrier as specified in [22].



Fig. 1. Proposed OFLL: (a) block diagram, (b) experimental setup.

The two lasers used for the experiments are a DFB laser Avanex A1905LMI and a Tunable Laser Diode (TLD) Santec TSL-210. In the experiment, the tunable laser and the DFB laser are employed as a master laser and slave laser having optical frequencies f_{ML} and f_{SL} , respectively, to produce the beat frequency, $\Delta f_{LO} = f_{ML} - f_{SL}$. The beat signal is provided by a 20GHz bandwidth photodetector (Discovery Semiconductor DSC30S) and is amplified by 20dB by a commercial RF amplifier (MiniCircuit ZX60-183). A power splitter (MiniCircuit ZFRSC-123-S) is used to tap the beat signal for monitoring. The output signal from the power splitter is mixed with a reference signal, \tilde{f}_{Ref} , at ~10GHz frequency provided by a signal generator (Agilent PSG E8267D). An error frequency, $f_e = \Delta f_{LO} - f_{Ref}$, is produced at the output of the RF mixer (MiniCircuit ZX05-153LH-S). The error signal, $V_{e}\cos(2\pi f_{e}t)$, is then passed through a variable frequency discriminator, where V_e is the amplitude of the error signal. In the frequency discriminator, the signal is divided into two equal parts by an RF splitter and recombined at a mixer (MiniCircuits SBL 48) after a variable delay line (Narda 3752) has deferred one part. Hence, we can write:

$$V_{1} = \frac{V_{e}}{2} \left[\cos(\omega_{e} t) \right] \quad (1)$$
$$V_{2} = \frac{V_{e}}{2} \left[\cos(\omega_{e} (t - \tau_{1})) \right] \quad (2)$$

where V_1 and V_2 are the signals combined in the mixer, $\omega_e = 2\pi(\Delta f_{LO} - f_{Ref})$, and τ_1 is the time delay. The resultant signal of the frequency discriminator at the output of the mixer, V_0 is [23]:

$$V_{0} = \frac{V_{\rm IF}}{\pi} \Big[\cos(2\pi f_{\rm IF}\tau_{\rm 1}) + \cos(2\pi f_{\rm IF}(2t-\tau_{\rm 1})) + \dots \Big] \quad (3)$$

The high-frequency term (the 2nd term in the bracket) is eliminated by a Low Pass Filter (LPF) (MiniCircuit SLP-50), called the loop filter. As a result, the LPF output produces a series of nulls when $\omega_e \tau_1 = (2n+1)\pi/2$, where $n = \pm 0, \pm 1, \pm 2$... The feedback system acts on the slave laser injection current and allows to actively control the emission frequency of the slave laser, so that a constant frequency difference between the slave laser is tuned by applying an external voltage to its injection current controller (ILX Lightwave LDC-3724). The conversion factor of this external voltage and the change of emission frequency of the slave laser, K_{SL} , is measured to be 31.25×10^{-2} MHz/mV.

A. Characterization of the RF Discriminator

Utilizing a passive RF component and realizing a delay of τ_1 =3ns, the discriminator output at the LPF as a function of the beat frequency is shown in Figure 2. The offset frequency between the first null from the reference frequency is given by $\Delta f = 1/2\tau_1$, where $1/\tau_1$ denotes the spacing of the nulls that the OFLL lockes on. The point A and B in Figure 2 is the nearest null to the reference frequency where the frequency to voltage conversion factor, K_a , is maximum and amounts to be 300mV/GHz. A higher τ_1 will reduce the Δf and will increase the slope at the nulls which ultimately will give a higher conversion of K_d . The various parameters and their values used in our analysis are summarized in Table I.



Beat frequency (GHz)

Fig. 2. Beat frequency as a function of error voltage for a delay of τ_1 =3ns.

TABLE I. VARIOUS LOOP PARAMETERS

Parameter	Value	Dimension
Total gain of the RF amplifiers	30dB	Ka
External voltage-to-frequency	31.25×10 ⁻²	K _{SL}
Conversion factor of the slave laser	MHz/mV	
Frequency-to-voltage conversion	300 mV/GHz	K _d
Factor of the discriminator		
Time constant of the current	300 µs	$ au_0$
Controller of the slave laser		
Time constant of the LPF	1.5805	τ_{2}

III. OPTICAL FREQUENCY LOCK LOOP ANALYSIS

The proposed OFLL in Figure 1 can be represented as a generic model for the feedback system and its s-domain representation is presented in Figure 3. In this model of the OFLL, the optical and the electrical connections are represented by doted and solid lines respectively. The optical field from the master and the slave laser are represented by E_{L1} and E_{L2} respectively. The fields are combined and put to a PD. The beat frequency is mixed with the reference frequency, f_{Ref} . The downconverted signal is then amplified by an RF amplifier having a gain of K_a . An RF frequency discriminator having a frequency-to-voltage conversion factor K_d converts the downconverted frequency into proportional error voltage. The error signal is passed through a LPF having a Laplace transform of the frequency response, F(s), to filter out the high frequency component from the error signal. The error signal is applied to the current controller of the slave laser. The external voltage-to-frequency conversion factor and the time constant of the slave laser controller are denoted by K_{SL} and τ_0 respectively.



Fig. 3. Linearized s-domain representation of the OFLL.

From the linearized s-domain representation of the OFLL the open-loop transfer function of the OFLL is defined as the product of the transfer functions of all the elements in the loop:

$$G(s) = K_{pd}K_{mix}K_{a}K_{FVC}K_{SL}F_{FM}(s) = KF_{FM}(s)$$
(4)

where $K = K_{pd}K_{mix}K_aK_{FVC}K_{SL}$ is the total gain. The closed-loop transfer function of the OFLL is [24]:

$$H(s) = \frac{K4\pi^{2}f_{r}^{2}}{s^{2} + s2\pi f_{c} + 4\pi^{2}f_{r}^{2}(1+K)}$$
$$= \frac{4\pi^{2}f_{n}^{2}}{s^{2} + s\zeta 2\pi f_{n} + 4\pi^{2}f_{n}^{2}(1+\frac{1}{K})}$$
(5)

where f_n and ζ represent the natural frequency and the damping factor of the loop and are expressed by:

$$\zeta = \frac{J_c}{f_n} \quad (6)$$
$$f_n = f_r \sqrt{K} \quad (7)$$

IV. EXPERIMENTAL RESULTS

In this section we will the stability of the OFLL will be investigated and analyzed. Moreover, some other functionalities of a carrier, namely the maximum tuning range and the maximum tuning rate (the speed at which the generated carrier can be tuned to a certain frequency) need to be investigated. Like any feedback system, the dynamics of these functionalities is determined by various parameters like loop gain and loop response. Also, the effect of various loop parameters (i.e. loop natural frequency and damping factor) on these functionalities of the OFLL will be investigated.

A. Locking Offset Range and Capture Range

The discriminator signal at the output of the LPF as a function of the beat frequency is shown in Figure 3. The error voltage is applied to the external tuning port of the current controller of the slave laser. This will ultimately change the emission frequency of the slave laser until the error voltage becomes zero. Hence, the generated carrier signal is eventually locked to the reference signal with a fixed offset frequency. The relation of the delay in the frequency discriminator and the offset frequency, as given in (4) is plotted in Figure 4. The measured values are also plotted and found to be close to the calculated values.



Fig. 4. The relation of the delay in the frequency discriminator and the offset frequency voltage for a delay of τ_1 =3ns.

From Figure 3, with τ_1 =3.3ns, the locking 150MHz offset frequency from the reference signal and the nulls are spaced by 300MHz. Figure 4 shows the relation of the delay in the frequency discriminator and the offset frequency. This offset frequency range also gives the range of frequencies from the reference signal at which the free-running carrier signal becomes locked, also called the capture range of the OFLL. In Figure 3, the error signal provides a capture range given by $\pm 1/2\tau_1 = \pm 150$ MHz.

B. Beat Frequency Tuning

Once the optically generated carrier signal is locked with the reference signal, the generated carrier signal can be tuned by simply varying the delay. The beat frequency is locked at 125, 135, 142, 151 and 166.6MHz offset frequency from the reference frequency. As given in (4), for delays of 3, 3.3, 3.5, 3.7, and 4ns the beat frequency will be locked at 166.6, 151, 142, 135, and 125MHz respectively. The beat frequency tuning for various delays is shown in Figure 5.

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C. Frequency Resolution

The frequency resolution is determined by the slope of the error signal at the locking point. As indicated by (4), a longer delay line τ_1 reduces the capture range but enhances resolution. In order to demonstrate the effect of delay on the sensitivity, for a fixed delay, the reference frequency was tuned from 9.66GHz to 9.54GHz, as shown in Figure 6. Depending on the delay, for example 3, 3.5, and 4ns, the beat frequency is also tuned with an offset frequency of 166.6, 142, and 125MHz respectively from the reference frequency. Figure 6 shows the frequency resolution of the locked signal for various delays. In the measurement the reference frequency was changed to demonstrate the frequency resolution. Moreover, the inset in Figure 6 shows the change of resolution due to the change of delay.



Fig. 6. Frequency resolution of the locked signal for various delays. The inset shows a highlighted portion of the measurement.

D. Loop Response Time

The step response provides an insight in loop response time and loop settling time. Note that the time taken for a feedback system to stay within 10% of its final value is called settling time. One widely used measure of the response speed of a feedback system is the time it requires to reach the 90% of its final value, which is called loop response time. With the help of (6) and (7), for a given loop bandwidth (1MHz in our experiment), the calculated loop response time values are plotted in Figure 7 for various loop natural frequencies and damping factors. From Figure 7 we can see that for a 1MHz loop bandwidth, the loop response becomes faster for a lower damping factor with higher settling time. The optimum value of the damping factor should be chosen by carefully considering both loop response time and settling time. From Figure 7 we can observe that there is a trade-off between the loop response time and settling time. With higher damping factor, the loop response time increases with decreasing settling time. The optimum value of the damping factor can be indicated as $\zeta = 1$ where both loop response time and settling time are optimum. The optimized loop natural frequency f_n is 1MHz.



Fig. 7. Loop response time for various natural frequencies and damping factors.

The step response measurement procedure is shown in Figure 8. The step signal from a vector signal generator (Agilent PSG E8267D) will introduce an external disturbance in the current controller of the slave laser (DFB laser) of the OFLL and due to this aggravation, the signal will suffer a temporary deviation from the desired frequency. The OFLL will recover the system into the former stable condition soon after the disturbance and this duration of the generated carrier signal from its unstable condition is measured by an oscilloscope (Agilent infinium 54854A) by synchronizing with the vector signal generator through an external trigger. Employing the values of the parameters in Table I, for a given value of gain, $K_a = 14$ dB, the natural frequency, f_n , and the damping factor, ζ , can be calculated using (7) and (8) as 1MHz and 1, respectively. For $K_a = 10$ dB the calculated values of the natural frequency and the damping factor become $f_n=0.62$ MHz and $\zeta = 1.7$ respectively.



From Figure 9, for $\zeta = 1$ and $\zeta = 1.7$ the speed of the response of the loop is found to be 0.35 and 0.85µs respectively. From Figure 9(a) it is evident that for a value of $\zeta = 1$ the calculated loop settling time is found to be 0.78µs which is slower than the 0.58µs (Figure 9(b)) for $\zeta = 1.7$ (called critical damping). The experimental results are in good agreement with the calculated values. From Figure 9 we can observe that there is a trade-off between loop response time and settling time. With higher damping factor, the loop response time increases with decrease of settling time. The optimum value of damping factor can be indicated as $\zeta = 1$ where both loop response time and settling time are also optimum. The optimized loop natural frequency, f_n is 1MHz.



Fig. 9. Loop settling time comparison, using step response, on calculated and measured values for damping factor of (a) 1 and (b) 1.7.

E. Frequency Response

The frequency response of the OFLL is plotted in Figure 10. The x-axis has been normalized by dividing the frequency by loop natural frequency f_n . The plot shows how the loop behaves in the frequency domain. Figure 10 shows the result of the frequency response of the OFLL for various damping factors. The frequency response of the loop in Figure 10 looks very similar to that of the frequency response of a low pass filter. This is what an OFLL acts in practice. If the frequency is equal to the natural frequency (i.e. when $f/f_n=1$) the oscillation becomes very large. This phenomenon is reflected in Figure 10(a) for $\zeta=1$. Larger damping factors have lower overshoot, i.e. better response, but also have long response time [29], as can be observed in Figure 10(b) for $\zeta=1.7$. Note that the higher frequency term in (3) needs to be filtered out, the response does it by keeping the loop bandwidth narrow. For the same values

of natural frequency and damping factor, the calculated result of the frequency response is presented in Figure 10. The experimental results are in good agreement with the calculated values. The step/impulse response of the system, a time domain response, can be converted into a frequency domain response by simply performing Fourier transformation. The calculated frequency response of the loop in Figure 10 is actually a time domain response (step response/impulse) of Figure 7. The time and frequency domain measurement values from Figure 7 and Figure 10 are in a good agreement.



Fig. 10. Experimental and calculated results of the frequency response of the OFLL, the x-axis is the normalized frequency and the y-axis is the response in dB.

F. Long Term Frequency Stability Analysis

Typically, a free-running microwave frequency, f_{beat} , deviates from the required frequency, f_{ideal} , with variable direction and rate of change. In order to express the long term frequency stability of a carrier, the Allan deviation σ_y is often used. To evaluate this frequency stability, σ_y is proven to be a valuable tool to quantify the stability of an optically generated microwave carrier [26]:

$$\sigma_{y}^{2}(\tau) = \frac{1}{2(M-1)} \sum_{i=1}^{M-1} \|\overline{y}_{i+1} - \overline{y}_{i}\|^{2} \quad (8)$$

where y_i is the *i*th of the *M* fractional frequency values averaged over the measurement (sampling) interval τ . The measured frequency stability of the generated carrier is expressed in Allan deviation in Figure 11 for both locked and free running conditions. For the free-running condition, for an increased averaging time of $\tau = 1$ s to $\tau = 1000$ s, the Allan deviation increases from 2.5×10^{-10} to 6×10^{-9} . However, by implementing the OFLL the corresponding improved long-term frequency stability could be 3×10^{-10} and 2.5×10^{-12} . The OFLL shows a 2400-fold improvement in the frequency stability at 1000s averaging time. A typical quartz oscillator (Wenzel 50104623E) has an Allan deviation frequency stability of 4×10^{-7} [27]. A typical atomic oscillator used as a clock source may provide a frequency stability of Allan deviation of 1×10^{-10} for 1s averaging time [28]. A radio frequency (RF) clock signal transmission over 100m via fiber link results in an Allan deviation of 1.3×10^{-10} for 1s averaging time [29]. The long term frequency stability of the presented work shows the superior performance of Allan deviation of 1×10^{-10} for an averaging time of 10^{3} s compared to OPLL setups involving integrated Phase-Frequency Detectors (PFD) [30].



Fig. 11. Allan deviation of the frequency stability of the OFLL under freerunning and locked conditions.

V. CONCLUSION

System analysis and experimental demonstration of an optically generated LC designed to comply with the requirements of the standard LC signal used in commercial LNBs have been investigated in this paper. The LC signal was used for the downconversion of the 10.7GHz to 12.75GHz signal received from a PA receiver. An OFLL was also implemented to stabilize the generated LC signal. Detailed analysis of the OFLL scheme has been presented. The loop filter and the loop gain of the OFLL should be chosen properly to make the feedback system stable and fast. It was also demonstrated that with an optimized gain of 14dB, the loop response time becomes 0.35μ s with a settling time of 11.5μ s. The presented results emphasize the effectiveness of the use of the OFLL in improving the long-term stability of the freerunning microwave carrier by a 2400-fold improvement in the frequency stability at 1000s averaging time. Previous research merely presents straightforward time domain measurements and lacks complete frequency domain measurements of the loop response. The presented research focused on this highly interesting topic. Both time and frequency domain measurement values are in a good agreement with each other. Moreover, the presented implementation shows batter performance in terms of the capture range of 150MHz compared to the very recent investigation of a few hundred kHz in [31].

ACKNOWLEDGMENTS

The authors acknowledge the support of the Smart Mix Program of the Dutch Ministry of Economic Affairs and the Dutch Ministry of Education, Culture and Science.

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